

**REMARKS**

Claims 1-26 are pending in this application. Claims 1, 6, 11, 16, 21 and 26 are rejected.

Claims 2-5, 7-10, 12-15, 17-20 and 22-25 are indicated to have allowable subject matter.

Reconsideration based on the following remarks is respectfully requested.

**I. The Claims Define Allowable Subject Matter**

**A. Claims 1 and 6**

The Office Action rejects claims 1 and 6 under 35 U.S.C. §103(a) as being unpatentable over U.S. Patent No. 6,340,959 to Inamori in view of U.S. Patent No. 6,147,672 to Shimamoto. The Applicant respectfully traverses these rejections.

Regarding claim 1, the Office Action states that Inamori teaches a display device which includes a first port through which still image data is input, a second port to which moving image data is input, a RAM, a first control circuit, a second control circuit and a LCD panel. The Office Action also states that Inamori fails to teach a reception circuit which differentially amplifies a differential signal output from the second port and creates the moving image data in a parallel state. The Office Action states that Shimamoto makes up for this deficiency.

The Applicant respectfully submits that the cited combination of Inamori and Shimamoto fails to disclose or suggest all of the subject matter recited in claim 1. Specifically, the cited combination fails to disclose or teach a first port through which still image data or a given command is input. Furthermore, the cited combination fails to mention the input of still image data as recited in claim 1. Hence, the cited combination fails to teach or suggest a RAM which stores still image data, a first control circuit which controls writing and reading of still image data, a second control circuit that independently controls the reading of display data of the still image data. The cited combination merely arguably

discloses a means of sending moving image data in the form of RGB data to a liquid crystal device and a television signal encoder.

Regarding claim 6, the Applicant respectfully submits that since claim 6 depends from claim 1, that claim 6 is allowable at least for the reasons stated regarding claim 1.

Withdrawal of the rejection of claims 1 and 6 under 35 U.S.C. §103(a) is respectfully requested.

**B. Claims 11 and 16**

The Office Action rejects claims 11 and 16 under 35 U.S.C. §103(a) as being unpatentable over Inamori and Shimamoto and further in view of U.S. Patent No. 6,370,603 to Silverman et al. Applicant respectfully traverses these rejections.

Regarding claims 11 and 16, Applicant respectfully submits that the cited combination of Inamori, Shimamoto and Silverman fail to disclose or teach all of the features recited in claims 11 and 16. Specifically, the cited combination fails to disclose or teach a first port through which still image data is input, a RAM which stores the still image data, a first control circuit which controls writing and reading of still image data, or a second control circuit that independently controls the reading display data of the still image data, as recited in claims 11 and 16. Withdrawal of the rejection of claims 11 and 16 under 35 U.S.C. §103(a) is respectfully requested.

**C. Claims 21 and 26**

The Office Action rejects claims 21 and 26 under 35 U.S.C. §103(a) as being unpatentable over Inamori and Shimamoto and further in view of U.S. Patent No. 6,137,466 to Moughami et al. Applicant respectfully traverses these rejections.

Regarding claim 21, the Office Action states that Inamori and Shimamoto teach all of the claim limitations of claim 1 except for the RAM incorporated driver, the column driver, a row driver. The Office Action then states that Moughami makes up with this deficiency.

Applicant respectfully submits that the cited combination of Inamori, Shimamoto and Moughami fail to teach or suggest all of the limitations recited in claim 21. Specifically, the cited combination fails to disclose the RAM incorporated driver as defined by claim 1, which provides plurality of first electrodes, specifically, the RAM incorporated driver as defined by claim 1 which includes a first port through which still image data is input. The cited portion of Moughami merely discloses a first driving circuit row drivers and row driver control and a data latch that is contiguous to the memory circuit.

Regarding claim 26 the Office Action states that Inamori teaches a CPU with supplies that command still image data and a moving image data to display unit. The Office Action cites Fig. 2, col. 9, lines 52-58. The Applicant respectfully submits that Inamori fails to disclose or teach an MPU (CPU) which supplies the still image data as recited in claim 26. The recited combination merely discloses an MPU which controls ON/OFF circuits which allows the television signal encoder to carry out image quality changes. The cited combination fails to disclose or teach an MPU (CPU) that supplies still image data as recited in claim 26. Withdrawal of the rejections of claims 21 and 26 is respectfully requested.

## **II. Conclusion**

In view of the foregoing, Applicant respectfully submits that this application is in condition for allowance. Favorable reconsideration and prompt allowance of claims 1-26 are earnestly solicited.

Should the Examiner believe that anything further would be desirable in order to place this application in even better condition for allowance, the Examiner is invited to contact Applicant's undersigned representatives at the telephone number listed below.

Respectfully submitted,



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Date: February 6, 2004

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